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(FILE 'USPAT' ENTERED AT 14:36:59 ON 12 JAN 1999)

L1 13397 S MEMORY AND FLASH
 L2 133 S L1 AND LOGICAL ADDRESS
 L3 83 S L2 AND PHYSICAL ADDRESS
 L4 58 S L3 AND ERAS?
 L5 44 S L4 AND SECTOR?
 L6 58 S L4 AND CONTROL?
 L7 58 S L6 AND WRIT?
 L8 55 S L7 AND REFERENCE?
 L9 15 S L8 AND ADDRESS(W) CONVER?

=> d cit 1-15

1. 5,844,841, Dec. 1, 1998, **Memory** system; Ken Takeuchi, et al., 365/185.03, 168, 185.22 [IMAGE AVAILABLE]
2. 5,812,814, Sep. 22, 1998, Alternative **flash** EEPROM semiconductor **memory** system; Hiroshi Sukegawa, 711/103, 5, 166 [IMAGE AVAILABLE]
3. 5,802,551, Sep. 1, 1998, Method and apparatus for **controlling** the **writing** and **erasing** of information in a **memory** device; Shinpei Komatsu, et al., 711/103 [IMAGE AVAILABLE]
4. 5,748,873, May 5, 1998, Fault recovering system provided in highly reliable computer system having duplicated processors; Hiroshi Ohguro, et al., 395/182.09; 371/47.1; 395/181, 182.08, 183.01 [IMAGE AVAILABLE]
5. 5,742,934, Apr. 21, 1998, **Flash** solid state disk card with selective use of an **address conversion** table depending on logical and physical sector numbers; Takayuki Shinohara, 711/103, 115, 202, 206 [IMAGE AVAILABLE]
6. 5,740,396, Apr. 14, 1998, Solid state disk device having a **flash memory** accessed by utilizing an **address conversion** table to convert sector address information to a physical block number; Colin Mason, 711/103; 365/185.33; 711/100, 173 [IMAGE AVAILABLE]
7. 5,724,285, Mar. 3, 1998, **Flash memory** PC card capable of refreshing data a predetermined time after the PC card is removed from a host; Takayuki Shinohara, 365/185.25; 235/492; 365/185.33, 228, 236; 395/182.04; 711/103 [IMAGE AVAILABLE]
8. 5,673,383, Sep. 30, 1997, Storage system with a **flash memory** module; Hiroshi Sukegawa, 395/182.06; 371/10.2 [IMAGE AVAILABLE]
9. 5,644,539, Jul. 1, 1997, Storage device employing a **flash**

memory; Hajime Yamagami, et al., 365/200, 185.33; 371/21.1, 21.2;
711/103 [IMAGE AVAILABLE]

10. 5,627,783, May 6, 1997, Semiconductor disk device; Shigenori
Miyachi, 365/185.33, 185.11, 218, 230.03 [IMAGE AVAILABLE]

11. 5,602,778, Feb. 11, 1997, Nonvolatile semiconductor memory
device with a row redundancy circuit; Tomoshi Futatsuya, et al.,
365/185.09, 185.23, 200 [IMAGE AVAILABLE]

12. 5,559,956, Sep. 24, 1996, Storage system with a flash memory
module; Hiroshi Sukegawa, 395/182.06, 182.05 [IMAGE AVAILABLE]

13. 5,548,557, Aug. 20, 1996, Nonvolatile semiconductor memory
device with a row redundancy circuit; Tomoshi Futatsuya, et al.,
365/201,
185.09, 200 [IMAGE AVAILABLE]

14. 5,530,673, Jun. 25, 1996, Flash memory control method
and information processing system therewith; Tsunehiro Tobita, et al.,
365/185.09, 185.04, 185.11, 185.22, 185.33, 201; 371/10.2, 21.1 [IMAGE
AVAILABLE]

15. 5,524,230, Jun. 4, 1996, External information storage system with a
semiconductor memory; Yoshinori Sakaue, et al., 711/103; 364/965.76,
966.4, DIG.1, DIG.2 [IMAGE AVAILABLE]